Applications are invited for a **Walk-in/Online interview** for the following post(s) in the project entitled, **" Design and Development of AI/ML Co-Processor and Post Quantum Cryptography Co-Processor (An Initiative towards Electronics System Design and Manufacturing in North-East Region)"** at the Department of Electronics and Electrical Engineering, IIT Guwahati.

Date: 28 June 2024 (Friday)

Time: 10.00 am

Venue: Walk-in Interview (E&ICT Conference Room, Technology Complex.) WebEx/Google Meet link will be sent by email to the shortlisted candidates for the online interviews.

Sl. No.	Project Staff Designation	Number of Vacancies	Pay (Rs.)	HRA (Rs.)	Medical (Rs.)	Total (Rs.)	Duration in months	Qualifications
1	Senior Project Engineer	1	75,000	13500	1250	89750	6 Months	(1) Ph. D in EC/EE/CS OR (2) MTech/ME in VLSI /Microelectronics/CS with 4 Years of relevant experience OR (3) BTech/BE in EC/EE/CS with 6 years relevant experience. Candidates having knowledge of chip signoff/tape-out and the commercial EDA tools and the VLSI Design flow are preferred.
2	Project Scientist	6	56000	10080	1250	67330	6 Months	(1)Ph.D in VLSI/Microelectronics/ CS, OR MTech/ME in VLSI/ Microelectronics/ CS with 2 Years of relevant experience OR (2) BTech/BE in EC/EE/CS with 4 years relevant experience. Candidates having knowledge of chip signoff/tape-out and the commercial EDA tools and the VLSI Design flow, RTL Design/Data Verification OR DFT/Synthesis OR Physical Design, and Signoff are preferred.

3	**Project Scientist-II**	1	56000	10080	1250	67330	6 Months	(1) PhD in VLSI/ Microelectronics/ CS, or MTech/ME in VLSI/ Microelectronics/ CS with 2 Years of relevant experience OR (2) BTech/BE in EC/EE/CS with 4 years relevant experience. Candidates having the knowledge of face detection /face recognition algorithm implantation using AI/ML are preferred.
4	**Associate Project Engineer**	2	35000	6300	1250	42550	6 Months	Master's degree in VLSI/Microelectronics/CS OR Bachelor's degree in EC/EE/CS with 3 years exp in RTL Design/Data Verification or DFT/Synthesis.
5	**Assistant Project Engineer**	4	29845	5373	1250	36468	6 Months	Bachelor's degree in EC/EE/CS with 1 year exp. in RTL Design/Data Verification OR DFT/Synthesis OR Physical Design, Signoff.
7	**Technical Staff**	1	46,000	8280	1250	55530	6 Months	(1) MTech/ME in Engineering with 03 years of experience OR (2) BTech/BE in Engineering with 05 years of experience OR (3) MSc/MA/MBA in any stream with 06 years of experience. Candidates having experience in management/administration would be preferred. It is desired to have excellent knowledge of English and the candidate is expected to have experience in managing workshops and training, writing letters, reports, etc.

How to apply and selection process:

Interested candidates may apply for the online/in-person interview at the links given below for the corresponding positions giving details of all educational qualifications, experience, contact address, phone no., email etc.

Form Link

https://forms.gle/AgiWFkAFB62Bpdiq9

Candidates need to upload the relevant documents in PDF format only at the registration links given above on or before **24 June 2024 (Monday) till 05.00 PM.** Candidates will be shortlisted based on the qualifications posted in the advertisement and, later only shortlisted candidates will be intimated by email separately to attend interviews.

Candidates applying for multiple posts will not be considered for selection.

<mark>**The last date for applying for the above-mentioned positions is 24 June 2024 (Monday) till 05.00 PM.**</mark>

Selection Process: The shortlisted candidates have to appear in the online or in-person interview. A choice must be made during the online form submission. Selection will be based on the performance of the candidate in the interview. The selection committee will decide on a suitable candidate after the interview. Candidates will not be sent any call letter separately.

For any clarification, contact the office of Dr. Gaurav Trivedi (Principal Investigator)

Email: piaipqcmeity@iitg.ac.in

ninelabsiitg@gmail.com

Phone: +91-7086509304/9864426791 (**Contact time: 10.00 AM – 6.00 PM**)

No campus accommodation will be available for the selected candidates.

No TA/DA will be paid to the candidates for appearing in the test and interview.

EEESPNMEIT00883xxGT008-ZBSA-2354   **Assistant Registrar (R&D)**