

Research and Development Section Indian Institute of Technology Guwahati Guwahati-781039, Assam

Applications are invited for a **Walk in /Online interview** for the following post(s) in the project entitled, "**Design and fabrication of 2D photonic crystal devices for data centre applications" on Contractual basis** at the department of Electronics and Electrical Engineering, IIT Guwahati.

Date: 26 June 2024 (Wednesday)

Time: 10 am

Venue: Online/Offline

SI. No.	Project Staff Designation	Number of Vacancie s	Pay Recommended (₹)	HRA (₹)	Medica I (₹)	Total Amount (₹)	Duration of Appointme nt in months	Qualifications
1	JRF GATE	01	31000(with GATE score) OR 25000 (without GATE Score)	4960(wit h GATE score) OR 4000(wit hout GATE Score)	1250	37,210(with GATE score) OR 30,250(without GATE Score)	6 months and extendabl e based on performa nce	1. B. Tech in EE / ECE / EEE / EIE or equivalent 2. M. Tech in VLSI / Photonics can also apply. 3. Should have basic understanding of semiconductor devices and optical fibre communication. 4. Knowledge in numerical tools like Synopsys, SILVACO, OptiWAVE, LUMERICAL, MATLAB, LabVIEW etc. is preferable.

How to apply and selection process:

Candidates have to appear in the Walk in Interview along with an application/CV on plain paper giving details of all educational qualifications, experience, contact address, phone no., E - mail etc. and submit photocopies of relevant documents at the time of interview on 26th June 2024 (Wednesday)) at 1000. Venue: online & Offline. Selection will be based on the performance of the candidate in the interview. Candidates will not be sent any call letter separately. Advance copy of CV must be sent to the Principal Investigator on or before 25th June 2024(1200Hrs).

Note:

- 1. Candidates will not be sent any call letter separately.
- 2. No TA/DA will be paid to the applicants for appearing in the interview.
- 3. No campus accommodation will be provided to the selected candidate.
- 4. Recruitment of selected candidate will be subjected to verification of original certificates.

For any clarification, contact: Ramesh Kumar Sonkar (Principal Investigator)

Email: sonkar@iitg.ac.in; Phone: 03612582535

xEEESPNSERB00875xRKS006

Asstt. Registrar(R&D)